-500mA / -12V Low Vce (sat) Digital transistors (with built-in resistors)

DTB543XE / DTB543XM

Applications

Inverter, Interface, Driver

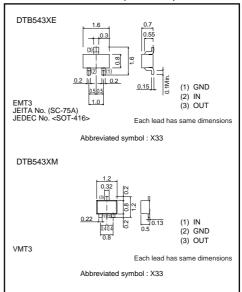
● Feature

- 1) VcE (sat) is lower than conventional products.
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- Only the on / off conditions need to be set for operation, making the device design easy.

Structure

PNP epitaxial plannar silicon transistor (Resistor built-in type)

●External dimensions (Unit : mm)



●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
	Symbol	DTB543XE DTB543XM	Offic
Supply voltage	Vcc	-12	V
Input voltage	Vin	-12 to +7	
Collector current *1	Ic (max)	-500	mA
Power dissipation *2	Po	150	mW
Junction temperature	Tj	150	ొ
Storage temperature	Tstg	-55 to +150	ဗ

^{*1} Characteristics of built-in transistor. *2 Each terminal mounted on a recom

Packaging specifications

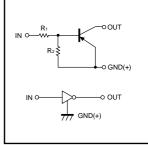
	Package	EMT3	VMT3
	Packaging type	Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTB543XE		0	-
DTB543XM		-	0

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
Input voltage	VI(off)	-	-	-0.3	V	Vcc=-5V, Io=-100μA		
	VI(on)	-2.5	-	-		Vo=-0.3V, Io=-20mA		
Output voltage	VO(on)	-	-70	-300	mV	Io/I:=-100mA / -5mA		
Input current	lı .	-	-	-1.4	mA	VI= -5V		
Output current	IO(off)	-	-	-500	nA	Vcc=-12V, Vi=0V		
DC current gain	Gı	140	-	-	-	Vo=-2V, Io=-100mA		
Transition frequency *	f⊤	-	260	-	MHz	Vc=-10V, Ie=5mA, f=100MHz		
Input resistance	R ₁	3.29	4.7	6.11	kΩ	_		
Resistance ratio	R2/R1	1.7	2.1	2.6	_	_		

^{*} Characteristics of built-in transistor.

Equivalent circuit



 $R_1=4.7k\Omega / R_2=10k\Omega$

^{*2} Each terminal mounted on a recommended land

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