

LAST USED REFERENCES

R178
 CT20
 C122
 U42
 JP4
 J9
 D8
 L8
 SW2
 RN3
 TP13
 MT5
 F1
 P2
 Y2

UNUSED REFERENCES

R87
 R97

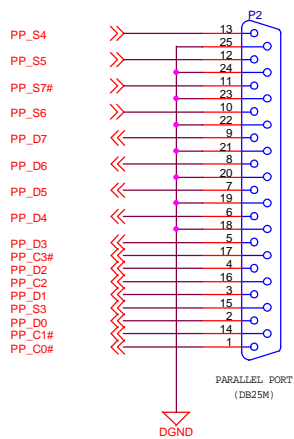
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

- PG TITLE
 1. COVER SHEET (THIS PAGE)
 2. CONNECTORS
 3. PARALLEL PORT
 4. JTAG
 5. VOLTAGE TRANSLATION
 6. CPLD
 7. CLOCKS
 8. DSP
 9. BUFFERS
 10. MEMORY DECODE
 11. MEMORY
 12. UART & RS232
 13. DAA
 14. SPEAKER & MICROPHONE
 15. AUDIO CODECS
 16. BSP MUX
 17. EXPANSION BUFFERS
 18. EXPANSION DB INTERFACE
 19. RESET CIRCUITRY
 20. LEDS/DIP SWITCHES
 21. INPUT POWER & GROUND
 22. DSP DECOUPLING CAPS

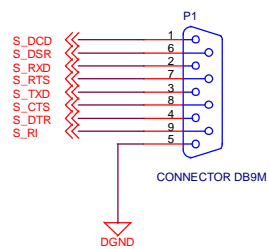
REVISION STATUS OF SHEETS											DWN	DATE	
REV	*	*										CHK	DATE
SH	21	22										ENGR	DATE
REV	*	*	*	*	*	*	*	*	*	*		ENGR-MGR	DATE
SH	11	12	13	14	15	16	17	18	19	20		QA	DATE
REV	*	*	*	*	*	*	*	*	*	*	NEXT ASSY	USED ON	DATE
SH	1	2	3	4	5	6	7	8	9	10	APPLICATION	RLSE	DATE

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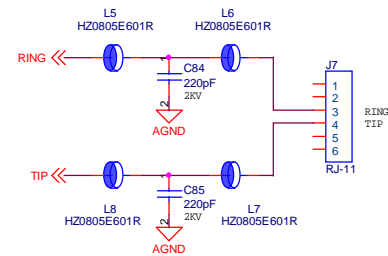
PARALLEL PORT



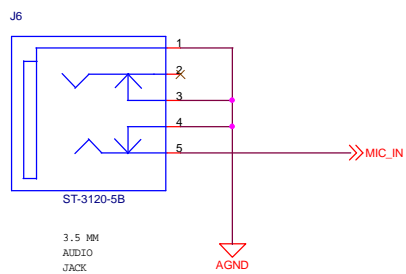
SERIAL PORT



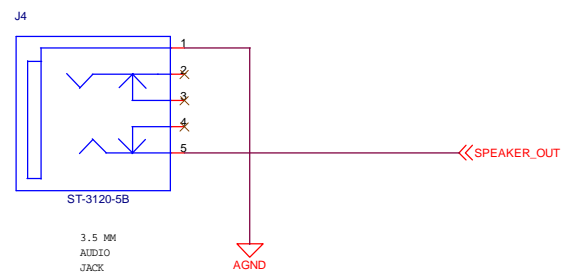
TELEPHONE LINE (RJ-11)



MIC IN



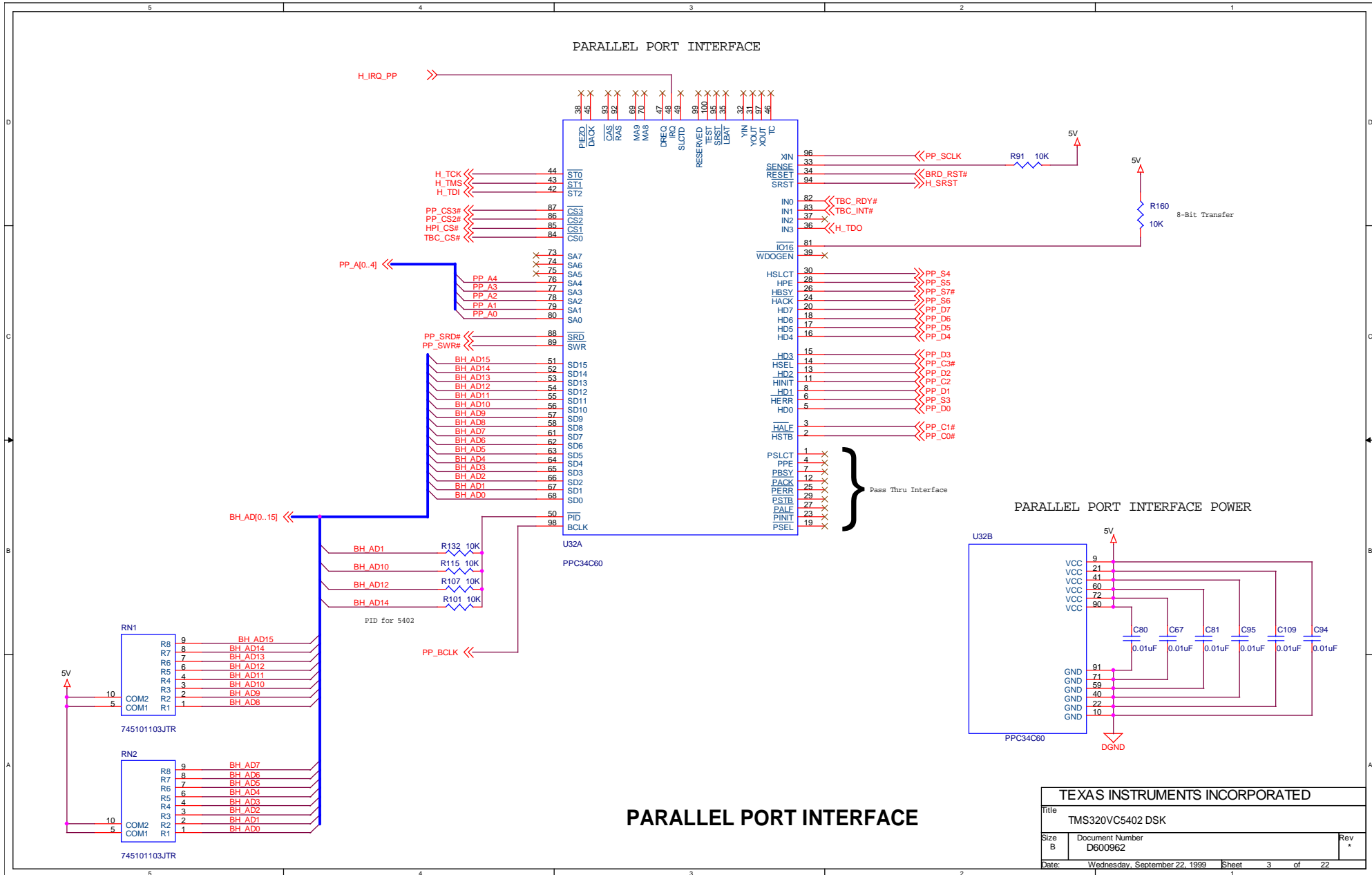
SPEAKER OUT



CONNECTORS

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PARALLEL PORT INTERFACE



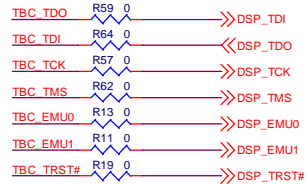
PARALLEL PORT INTERFACE

PARALLEL PORT INTERFACE POWER

TEXAS INSTRUMENTS INCORPORATED

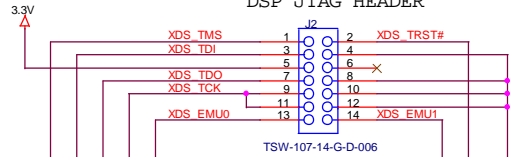
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JTAG PASS THRU

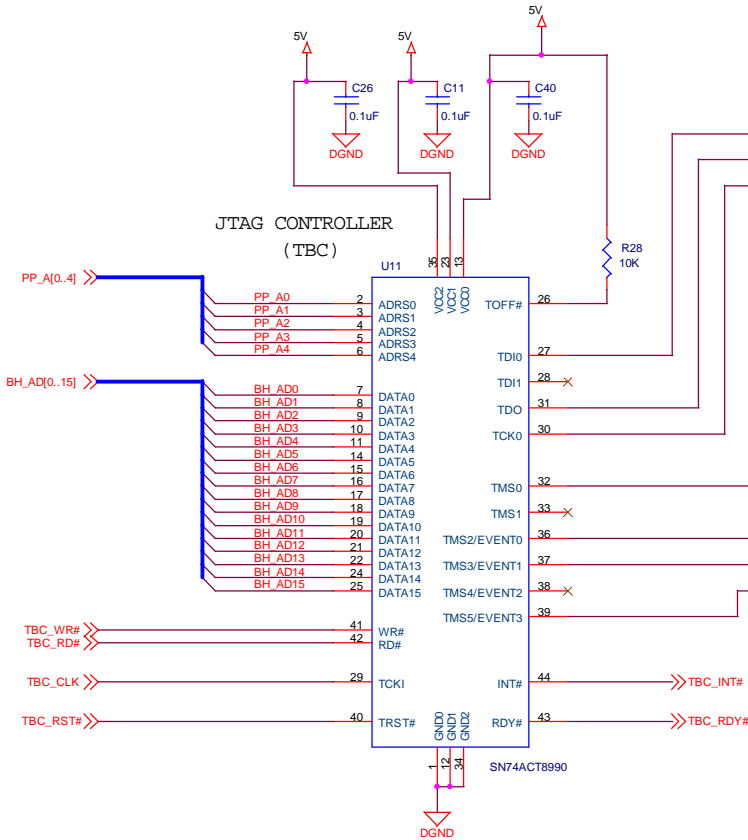


NOTE: Resistors only installed if mux's not installed.

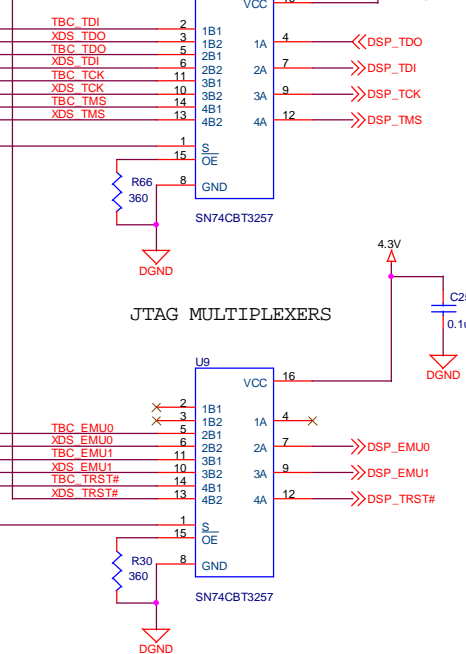
DSP JTAG HEADER



JTAG CONTROLLER (TBC)

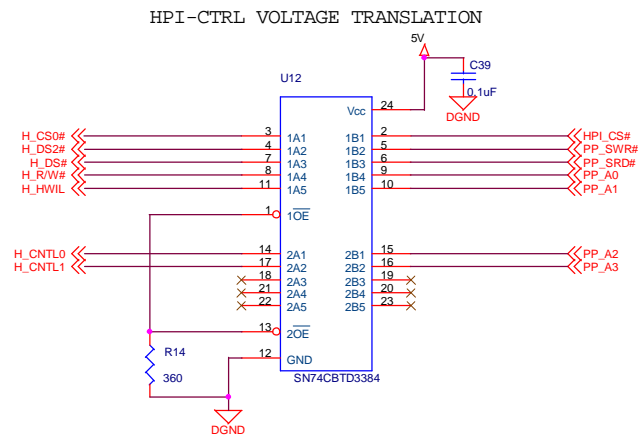
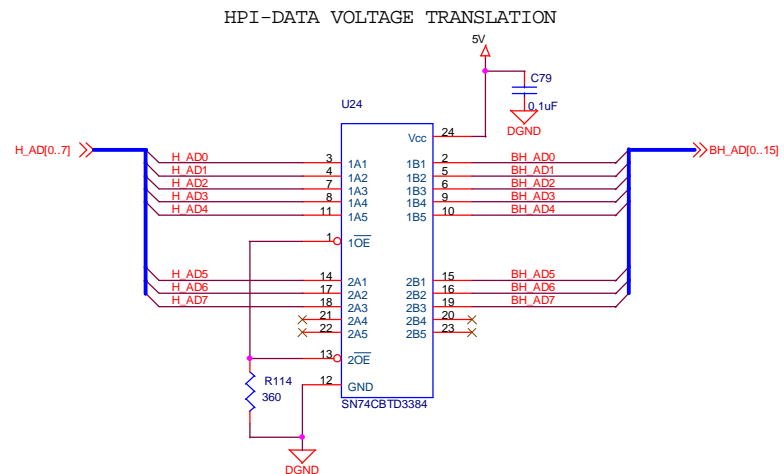
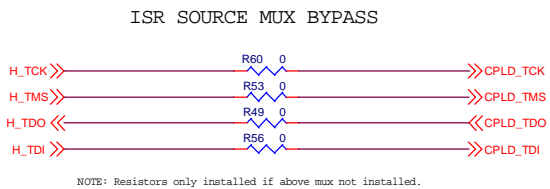
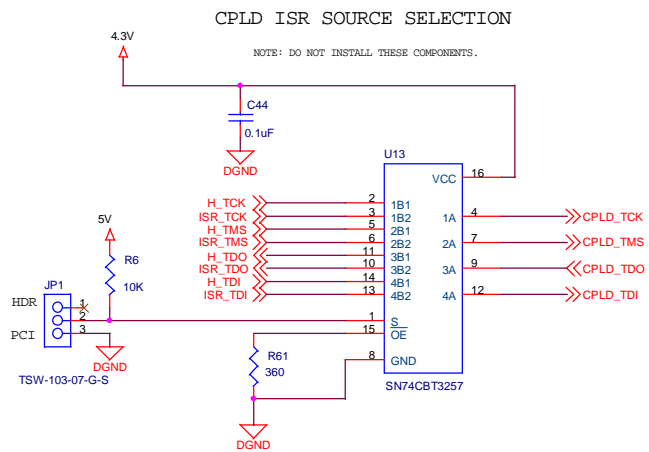
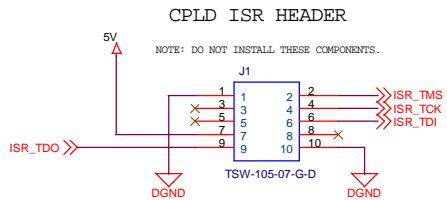


JTAG MULTIPLEXERS



JTAG

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5V / 3.3V TRANSLATION & CPLD HEADER

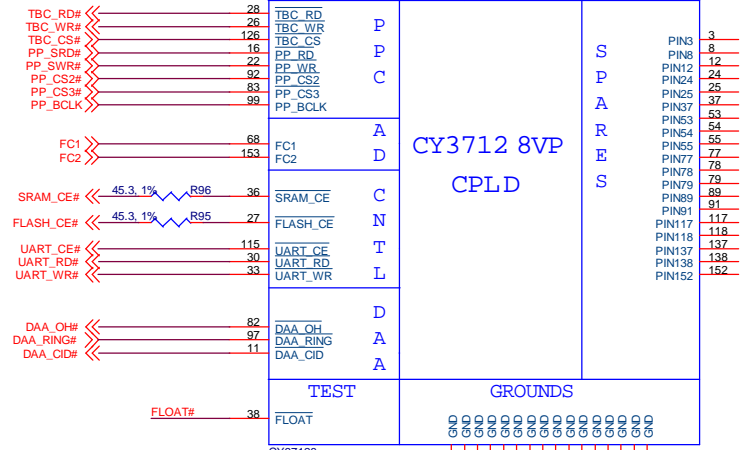
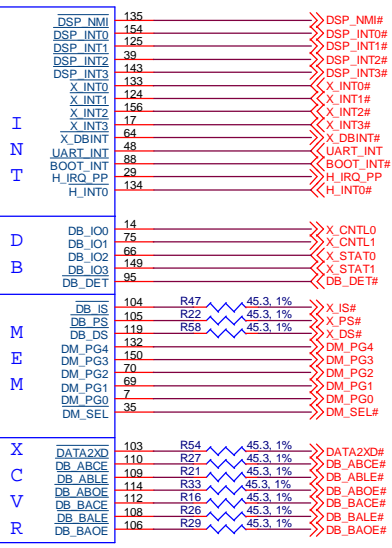
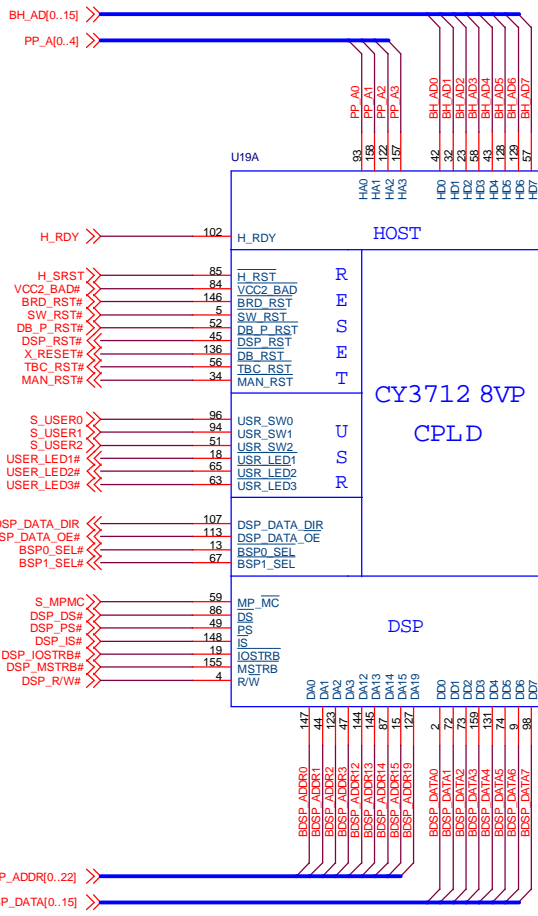
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TMS320VC5402 DSK

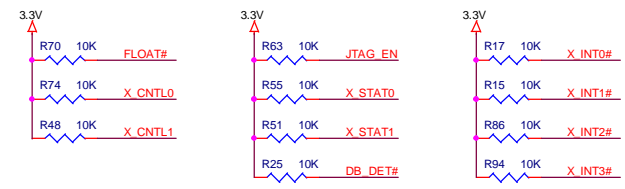
Size B Document Number D600962 Rev *

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MEMORY DECODING & GLUE LOGIC



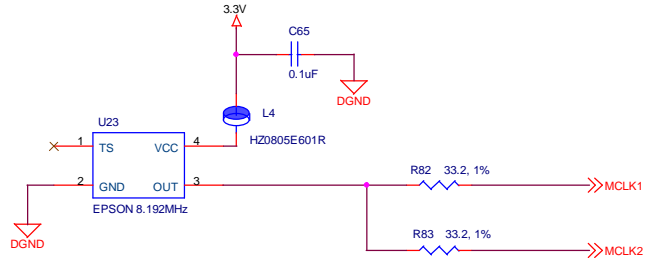
CPLD PULL-UP RESISTORS



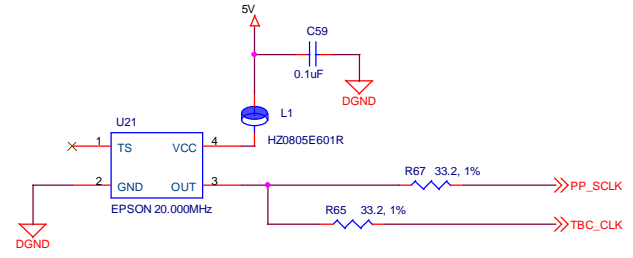
CPLD

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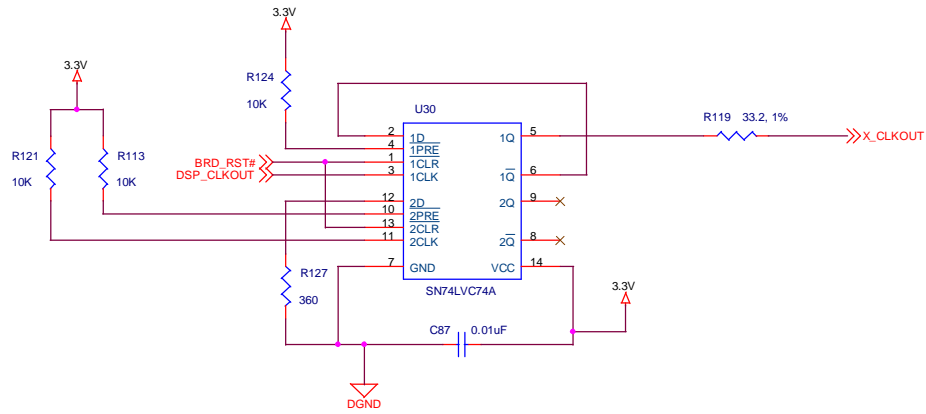
8.192 MHz OSCILLATOR



20.000 MHz OSCILLATOR

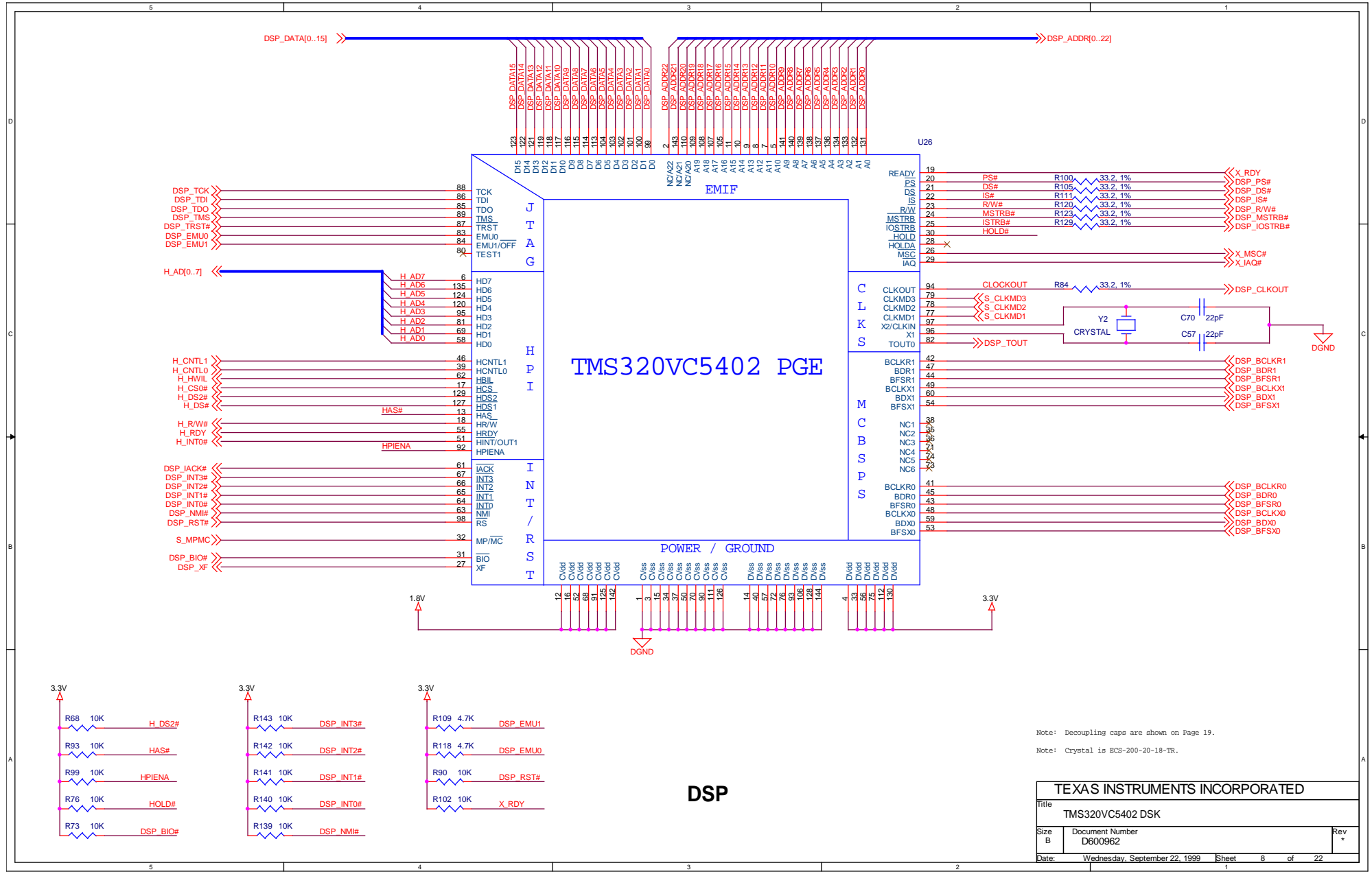


DAUGHTER BOARD CLOCK



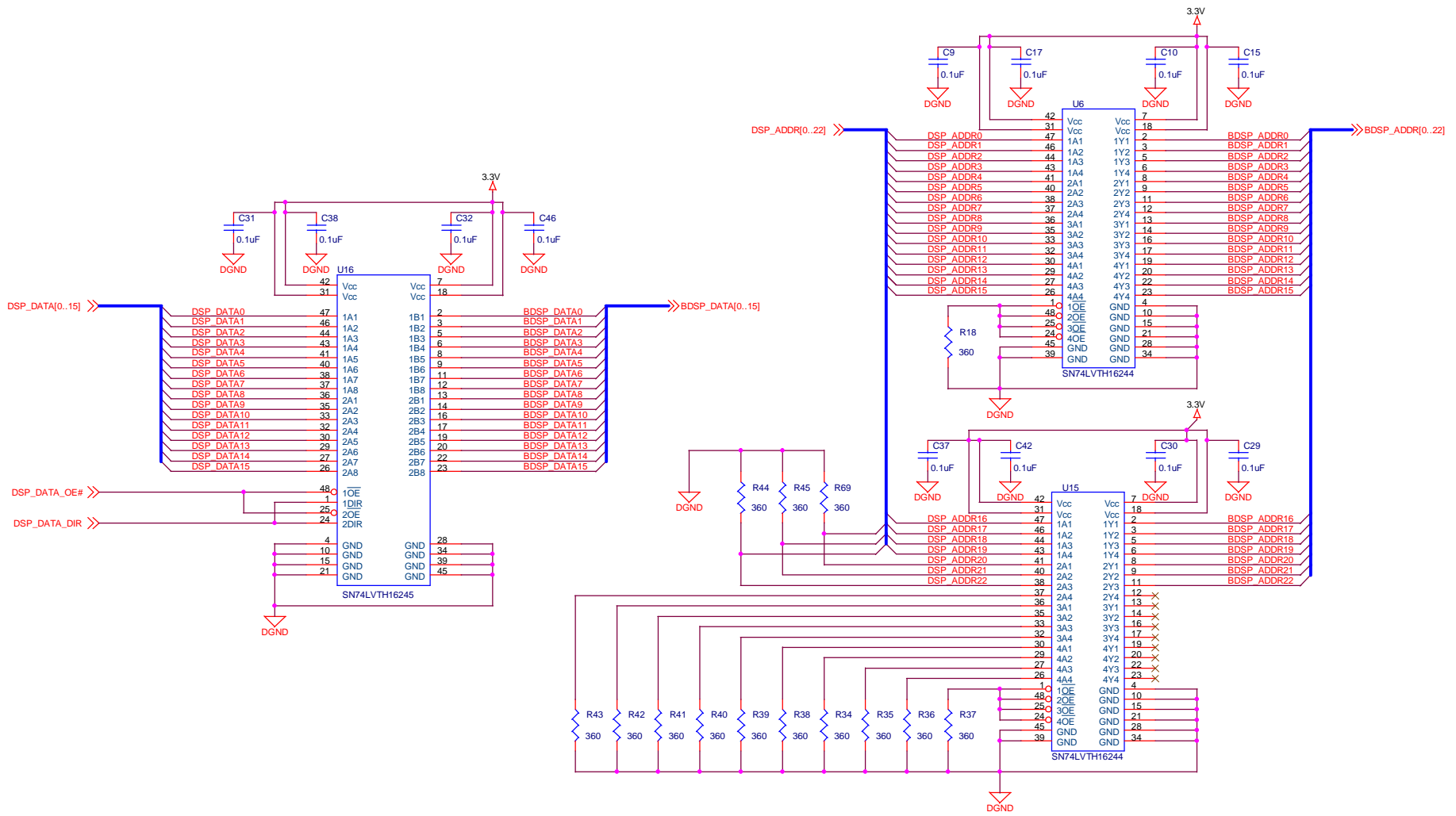
CLOCKS

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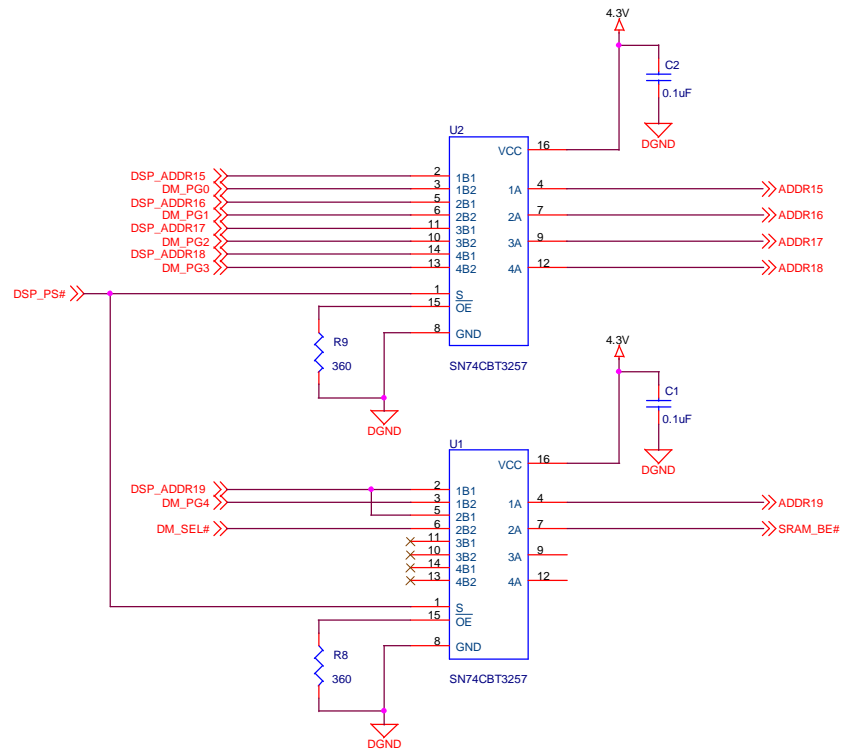
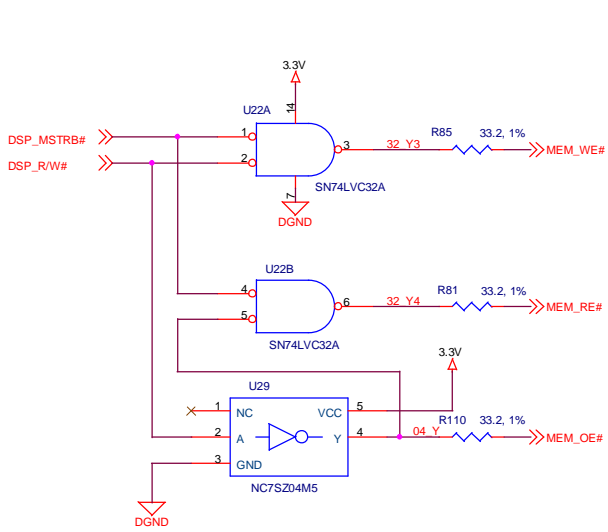
Note: Decoupling caps are shown on Page 19.
 Note: Crystal is ECS-200-20-18-TR.

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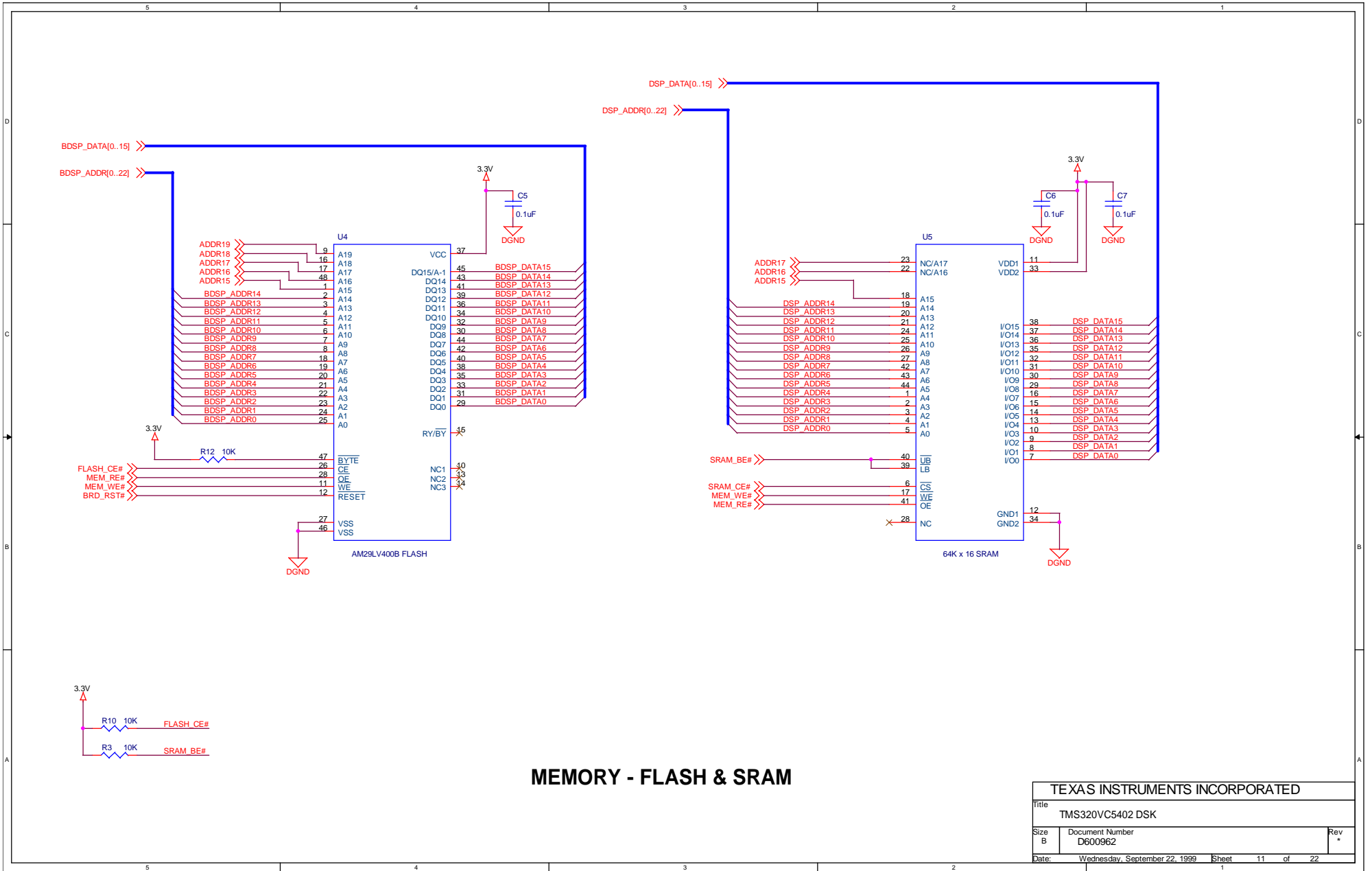
BUFFERS

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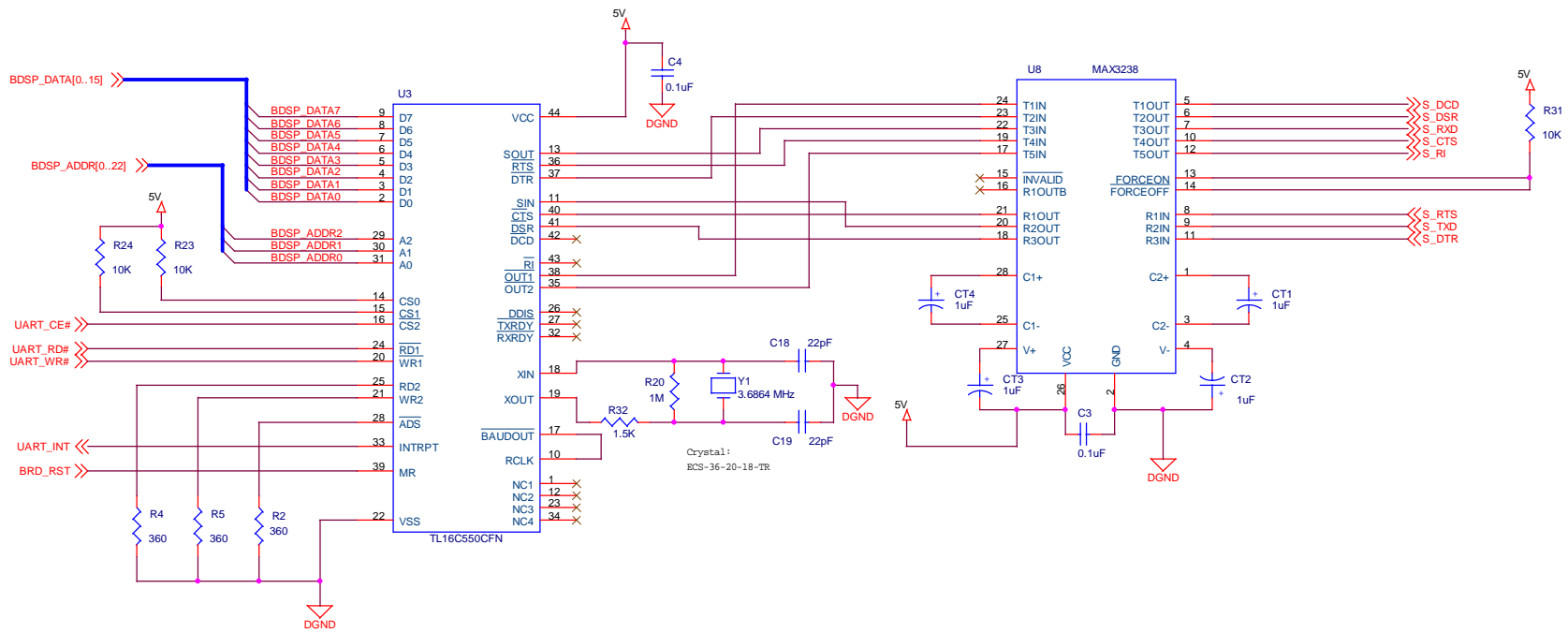


MEMORY DECODE

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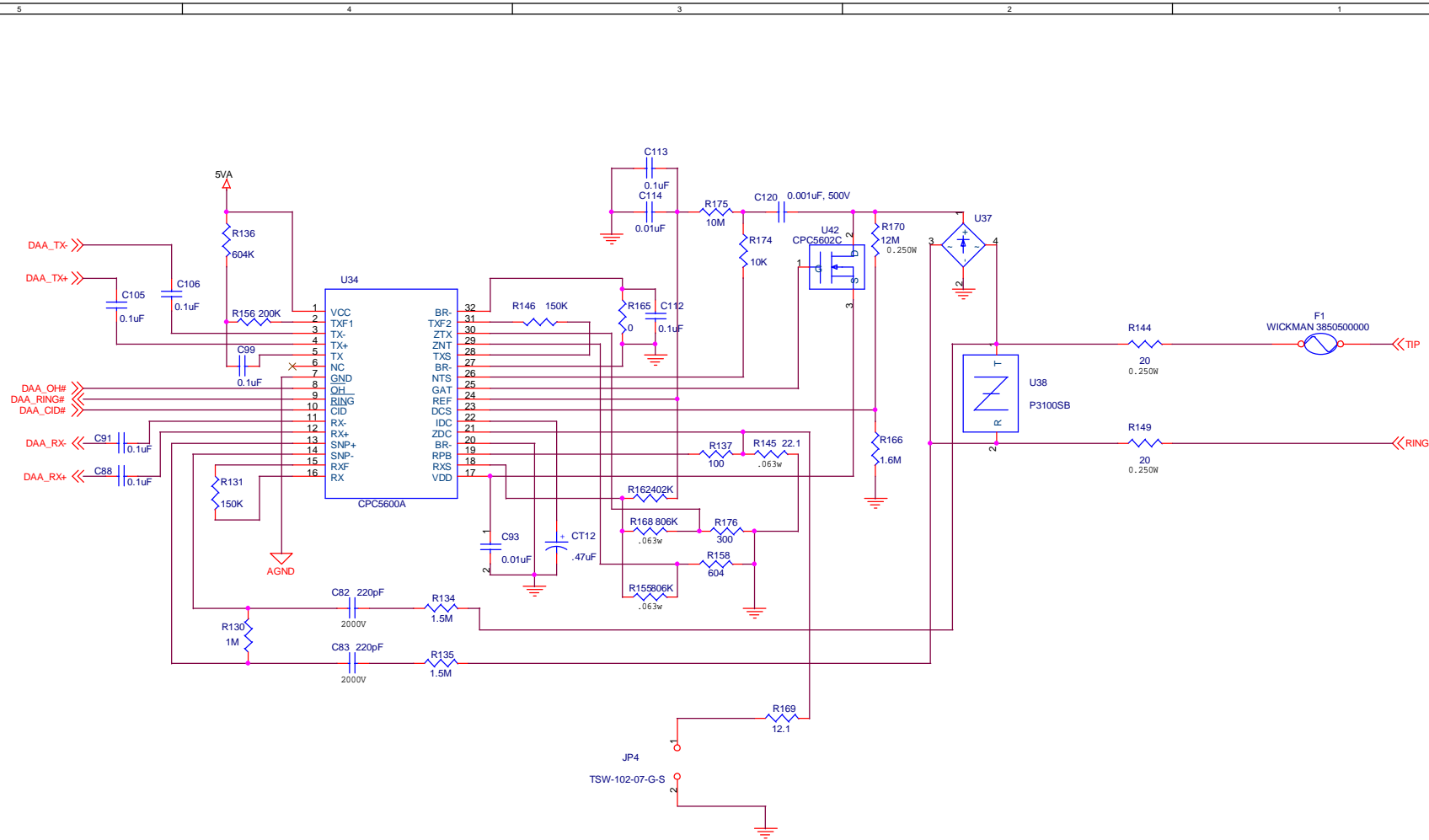



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UART & RS232

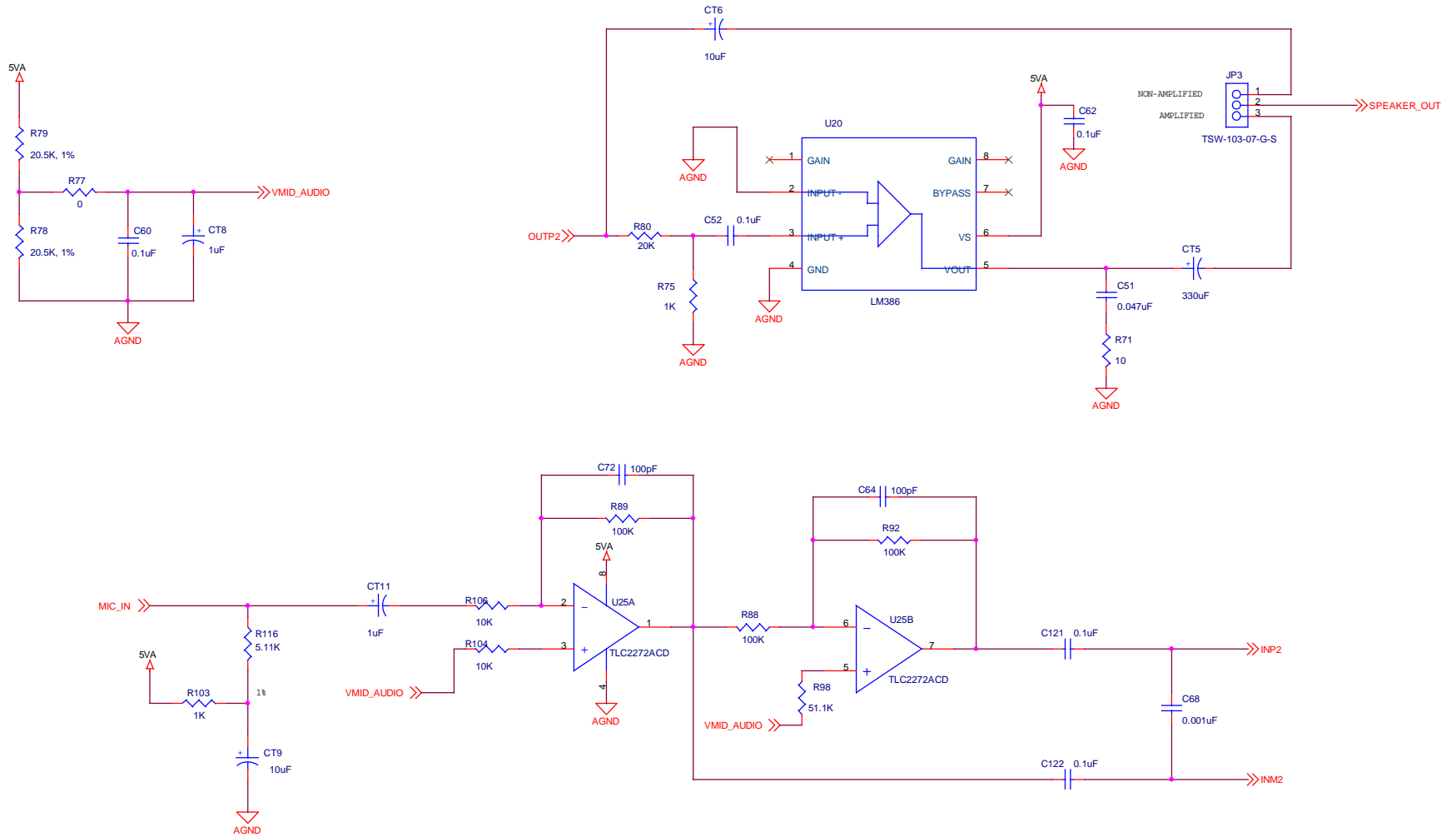
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Notes:
 All resistors are 100W unless otherwise noted.
 means "Floating" Ground.

DATA ACCESS ARRANGEMENT

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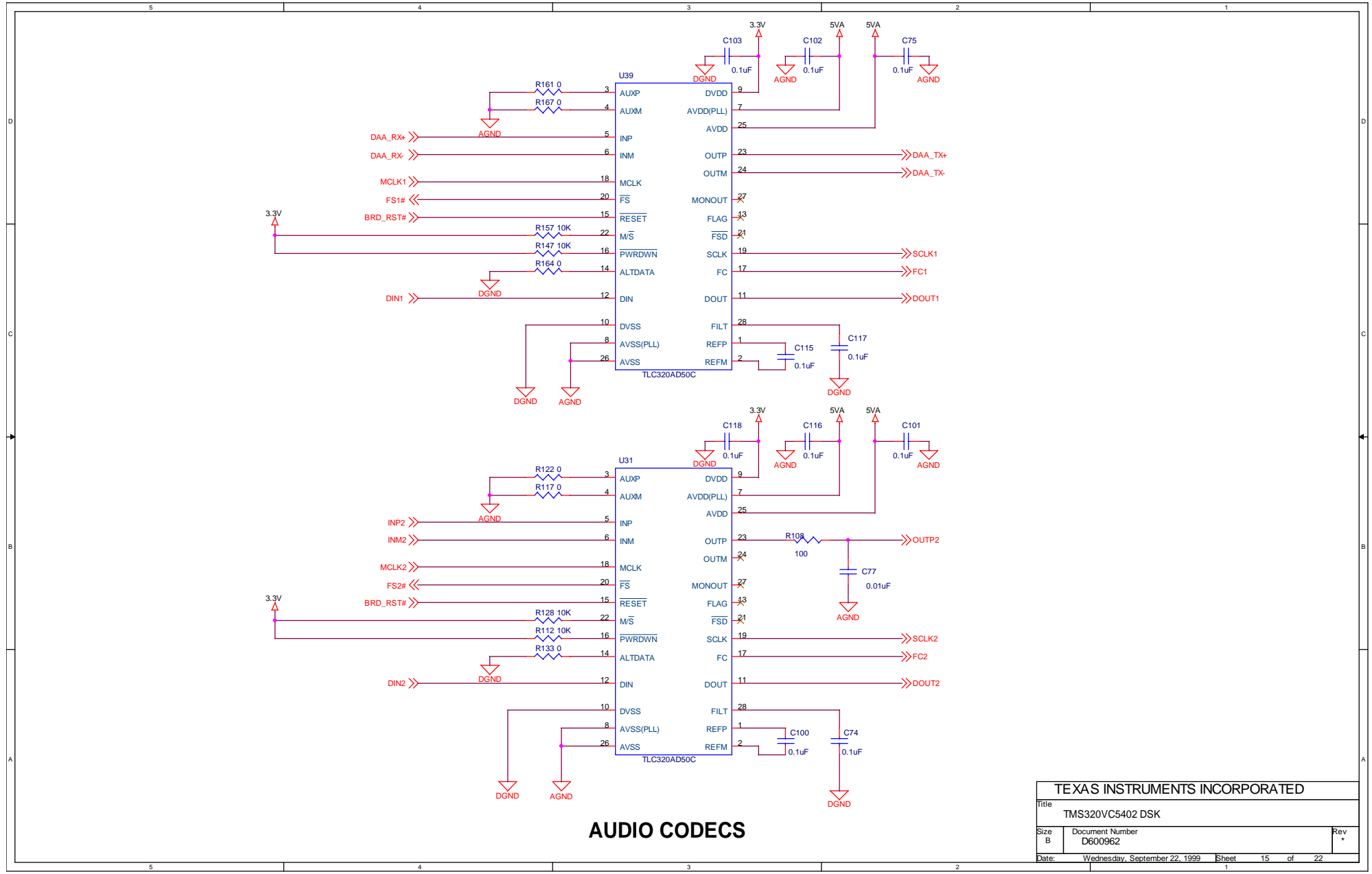
SPEAKER / MICROPHONE

TEXAS INSTRUMENTS INCORPORATED

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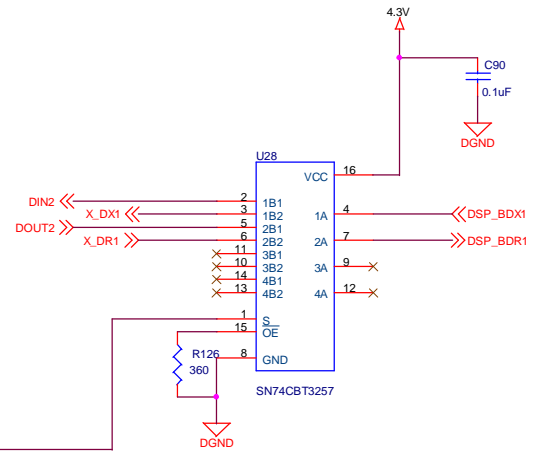
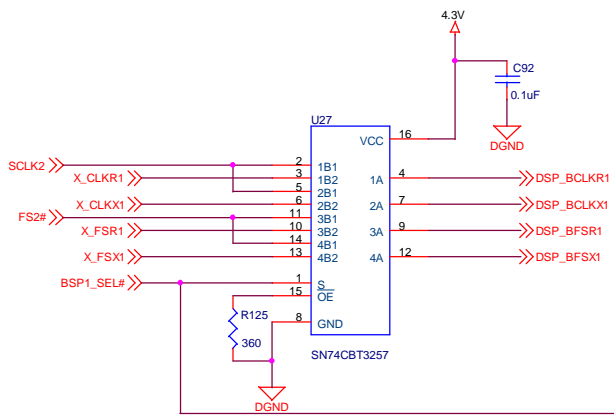
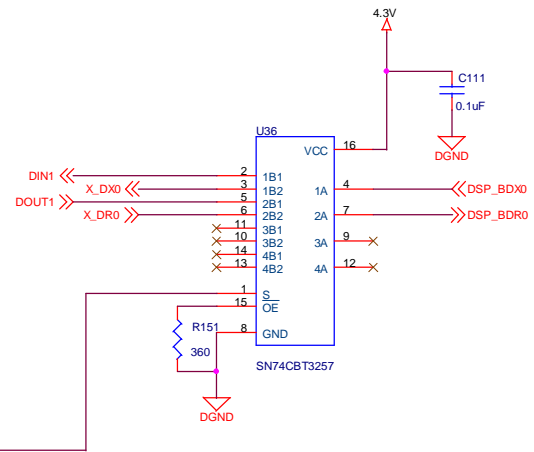
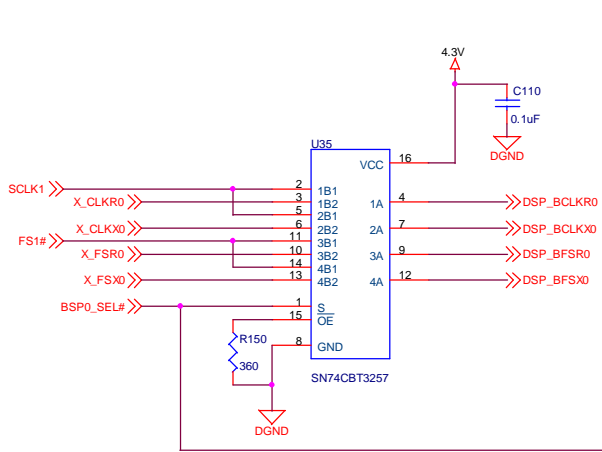
Size B Document Number D600962 Rev *

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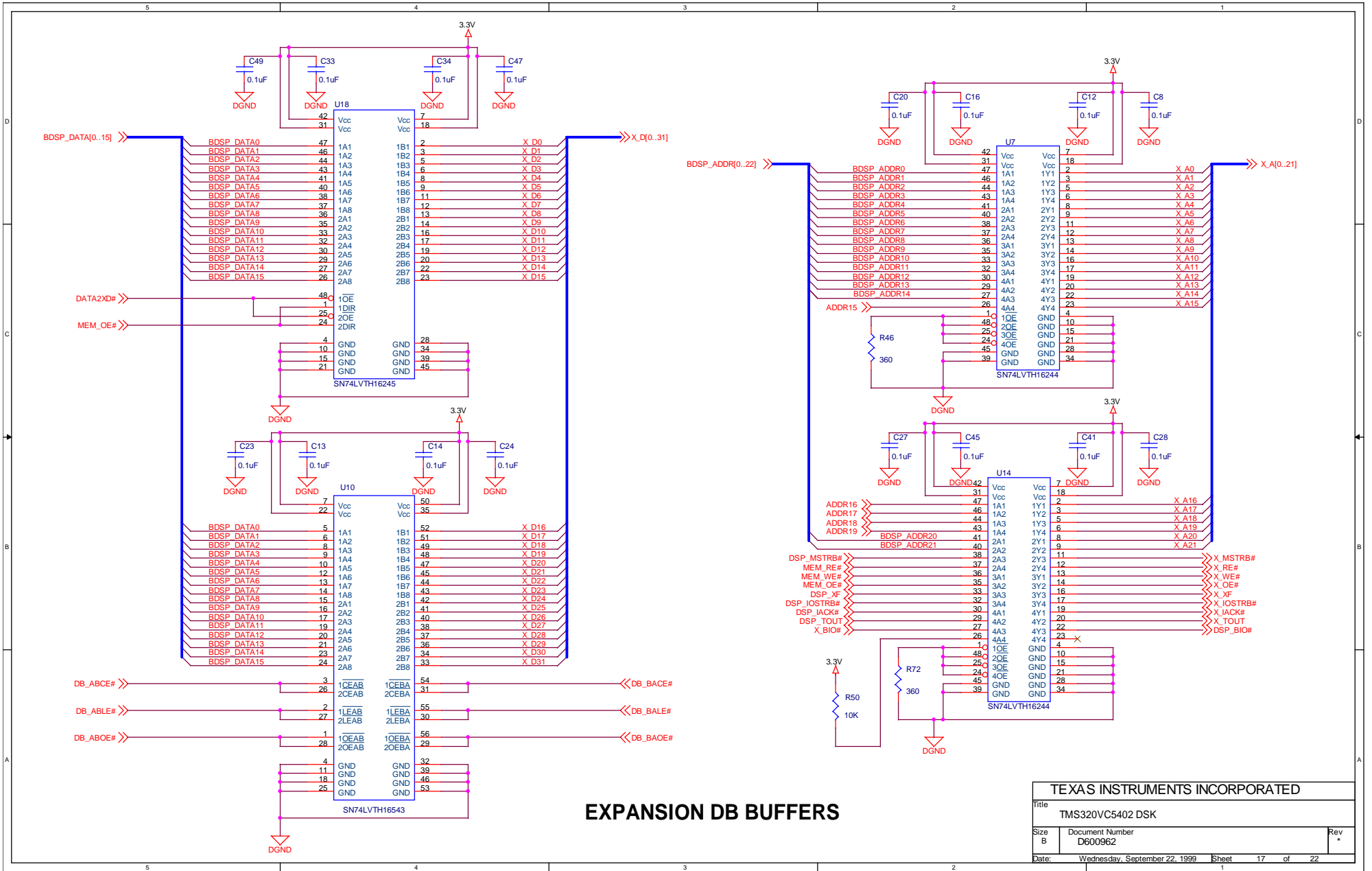
AUDIO CODECS

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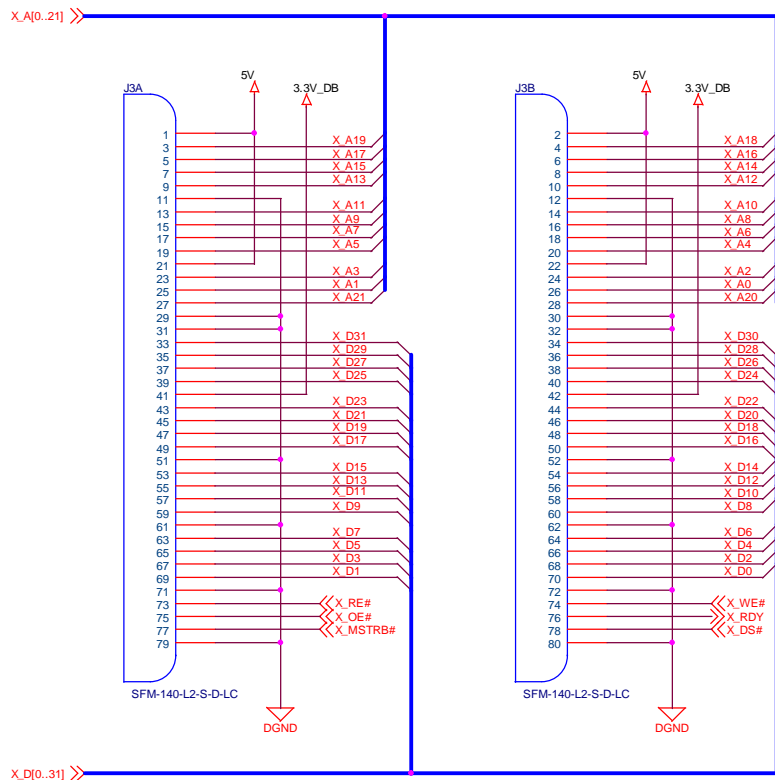
BSP MUX

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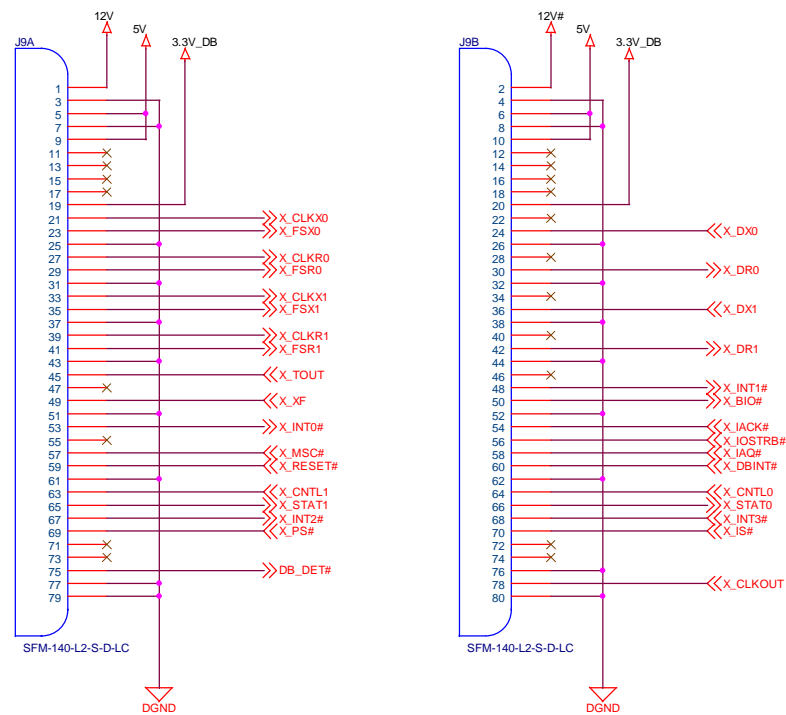


EXPANSION DB BUFFERS

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MEMORY INT ERFACE

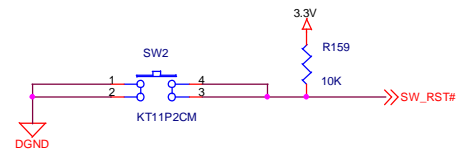


PERIPHERAL I NTERFACE

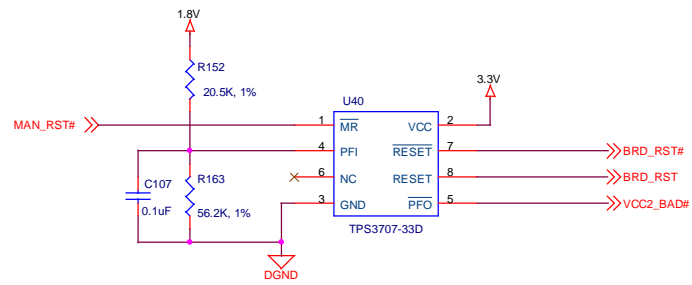
EXPANSION DB INTERFACES

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PUSHBUTTON RESET



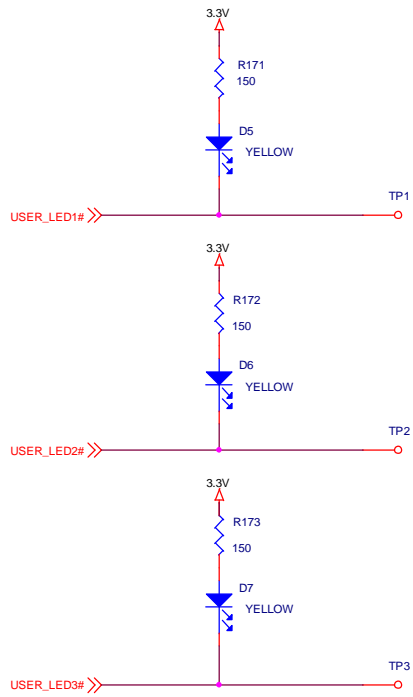
PROCESSOR VOLTAGE SUPERVISOR



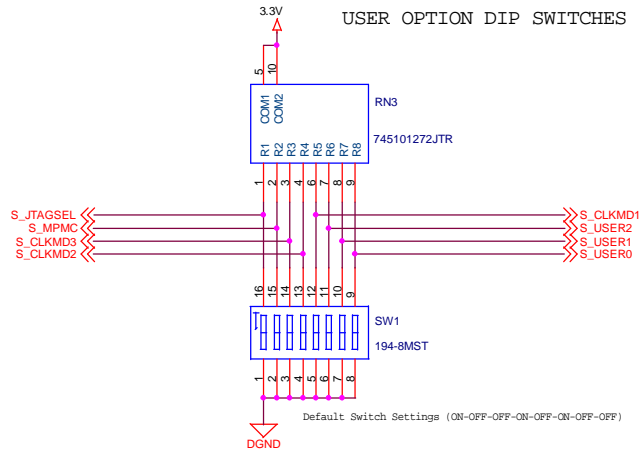
RESET

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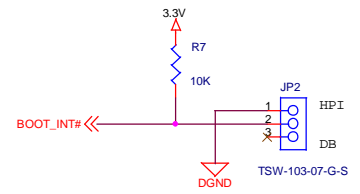
USER LEDS



USER OPTION DIP SWITCHES



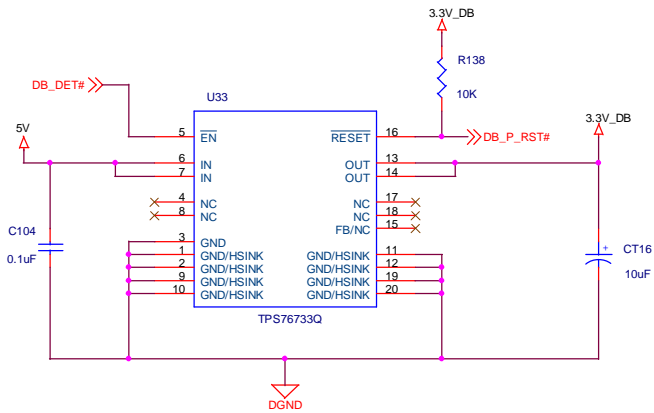
DSP BOOT SOURCE SELECTION



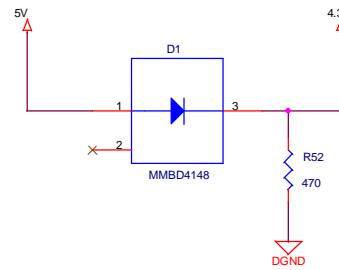
LEDS/SWITCHES

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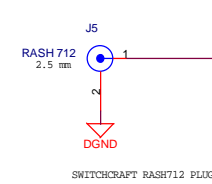
3.3V DAUGHTERBOARD VOLTAGE



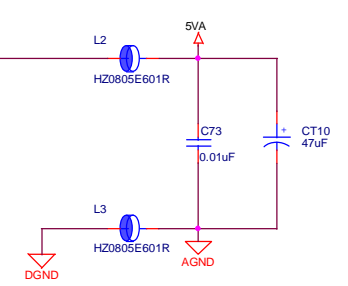
3257 VOLTAGE DIVIDER



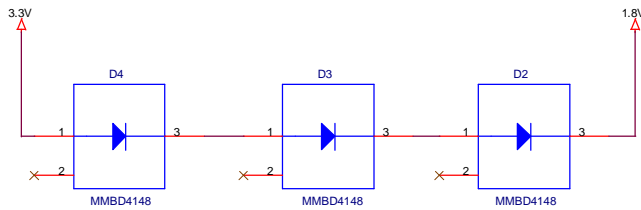
EXTERNAL POWER PLUG



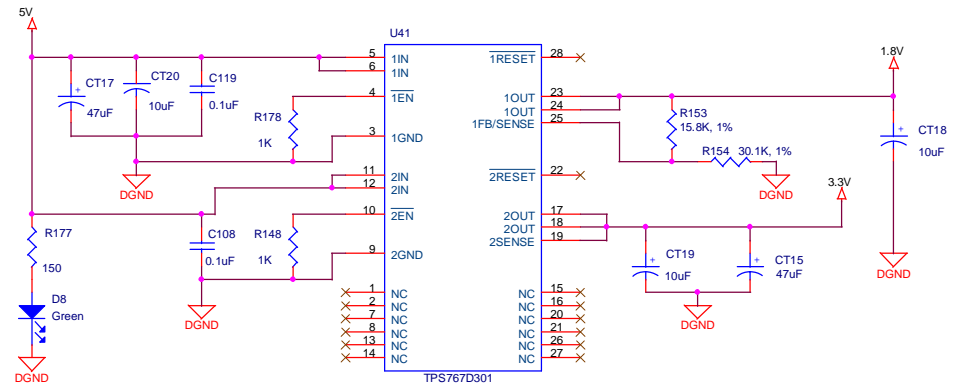
+5V & +5VA POWER



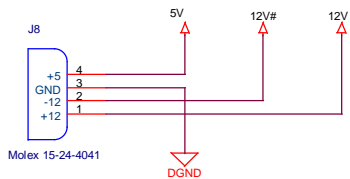
DSP CORE & I/O DIFFERENTIAL VOLTAGE PROTECTION



+3.3V & +1.8V DIGITAL VOLTAGE REGULATOR



ALTERNATE EXTERNAL POWER (DO NOT POPULATE)

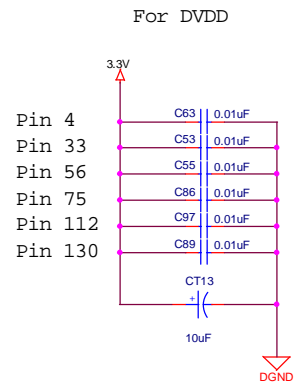
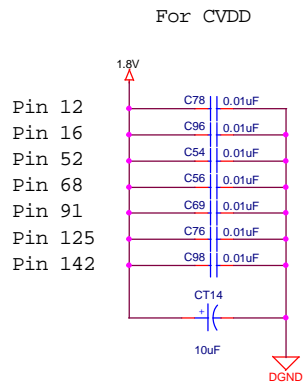


INPUT POWER

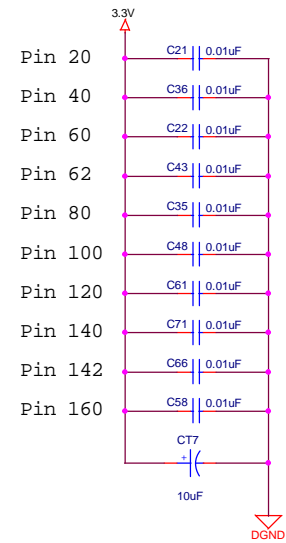
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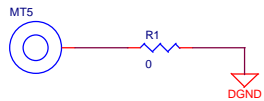
DSP DECOUPLING CAPS



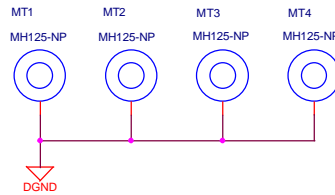
CPLD DECOUPLING CAPS



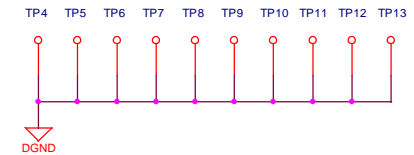
CHASSIS GROUND



DAUGHTERBOARD STANDOFF GROUNDING



GROUND TEST POINTS



DECOUPLING CAPS & GROUND

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